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STATUTORY INSTRUMENTS

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**2018 No. 1052**

**EXITING THE EUROPEAN UNION  
DESIGNS**

**The Design Right (Semiconductor Topographies)  
(Amendment) (EU Exit) Regulations 2018**

*Sift requirements satisfied*                      *5th September 2018*

*Made*       -       -       -       -                      *9th October 2018*

*Laid before Parliament*                      *9th October 2018*

*Coming into force in accordance with regulation 1*

The requirements of paragraph 3(2) of Schedule 7 to the European Union (Withdrawal) Act 2018<sup>(1)</sup> (relating to the appropriate Parliamentary procedure for these Regulations) have been satisfied.

The Secretary of State makes the following Regulations in exercise of the powers conferred by section 8(1) of, and paragraph 21(b) of Schedule 7 to, that Act<sup>(2)</sup>.

**Citation and commencement**

1. These Regulations may be cited as the Design Right (Semiconductor Topographies) (Amendment) (EU Exit) Regulations 2018 and come into force on exit day.

**Amendment of the Design Right (Semiconductor Topographies) Regulations 1989**

2.—(1) Regulation 4 (qualification) of the Design Right (Semiconductor Topographies) Regulations 1989<sup>(3)</sup> is amended as follows.

(2) In paragraph (2) for “another” substitute “a”.

(3) In paragraph (4)—

(a) for “every member State of the European Economic Community” substitute “the United Kingdom”;

(b) for “any member State” substitute “the United Kingdom”;

(c) omit from “and subsection (4)” to the end of the sentence.

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<sup>(1)</sup> 2018 c.16.

<sup>(2)</sup> See section 20(1) of that Act for a definition of “Minister of the Crown”.

<sup>(3)</sup> S.I. 1989/1100, as amended by S.I. 2006/1833 and 2008/1434.

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**Status:** This is the original version (as it was originally made). This item of legislation is currently only available in its original format.

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9th October 2018

*Sam Gyimah*  
Minister of State for Universities, Science,  
Research and Innovation  
Department for Business, Energy and Industrial  
Strategy

## EXPLANATORY NOTE

*(This note is not part of the Regulations)*

These Regulations amend the Design Right (Semiconductor Topographies) Regulations 1989 in order to address failures of those Regulations to operate effectively and other deficiencies (in particular under sections 8(2)(a) and (g)) of the European Union (Withdrawal) Act 2018 (c.16) arising from the withdrawal of the United Kingdom from the European Union.

An impact assessment has not been published for this instrument as no, or no significant, impact on the private, public and voluntary sectors is foreseen. An explanatory memorandum is available alongside this instrument on the Legislation UK website at [www.legislation.gov.uk](http://www.legislation.gov.uk) and it may also be obtained from the Intellectual Property Office, Concept House, Cardiff Road, Duffryn, Newport, NP10 8QQ.