
STATUTORY INSTRUMENTS

2006 No. 1833

The Design Right (Semiconductor Topographies) (Amendment) Regulations 2006

Citation and commencement

1. These Regulations may be cited as the Design Right (Semiconductor Topographies) (Amendment) Regulations 2006 and shall come into force on 1st August 2006.

Amendment to the Design Right (Semiconductor Topographies) Regulations 1989

2. The Design Right (Semiconductor Topographies) Regulations 1989⁽¹⁾ shall be amended as follows.

3. For regulation 4(2) there shall be substituted—

“(2) Part III of the Act has effect as if for section 217(3) there was substituted the following—

“(3) In this section “qualifying country” means—

- (a) the United Kingdom,
- (b) another member State,
- (c) the Isle of Man, Gibraltar, the Channel Islands or any colony,
- (d) a country listed in the Schedule to the Design Right (Semiconductor Topographies) Regulations 1989.””

4. For the Schedule to those Regulations (additional classes of qualifying persons)⁽²⁾ there shall be substituted the Schedule (qualifying countries) set out in Schedule 1 to these Regulations (new schedule for the Design Right (Semiconductor Topographies) Regulations 1989).

Revocations

5. The instruments set out in Schedule 2 (revocations) are revoked to the extent specified.

Sainsbury of Turville
Parliamentary Under Secretary of State for
Science and Innovation
Department of Trade and Industry

10th July 2006

⁽¹⁾ [SI 1989/1100](#).

⁽²⁾ This Schedule has been substituted or amended by [SI 1993/2497](#), [1992/400](#), [1991/2237](#), [1990/1003](#) and [1989/2147](#). All of these Instruments are revoked by this Instrument.