
STATUTORY INSTRUMENTS

1989 No. 2147

DESIGNS

**The Design Right (Semiconductor
Topographies) (Amendment) Regulations 1989**

Made - - - - *2nd November 1989*
22nd November
Laid before Parliament *1989*
Coming into force - - *1st December 1989*

The Secretary of State, being designated⁽¹⁾ for the purposes of section 2(2) of the European Communities Act 1972⁽²⁾ in relation to the conferment and protection of exclusive rights in the topographies of semiconductor products, in exercise of the powers conferred on him by the said section 2(2) hereby makes the following Regulations:—

1. These Regulations may be cited as the Design Right (Semiconductor Topographies) (Amendment) Regulations 1989 and shall come into force on 1st December 1989.

2. The Schedule to the Design Right (Semiconductor Topographies) Regulations 1989⁽³⁾ shall be amended as follows—

- (a) in Part II thereof (which, in relation to the countries named, specifies citizens, subjects, habitual residents, firms and bodies corporate as additional classes of qualifying persons), there shall be included a reference to Sweden; and
- (b) in Part III thereof (which, in relation to the countries named, specifies citizens, subjects and habitual residents only as additional classes of qualifying persons), there shall be omitted the reference to Sweden.

Department of Trade and Industry
2nd November 1989

Douglas Hogg
Minister of State for Industry and Enterprise,

(1) S.I.1987/448.
(2) 1972 c. 68.
(3) S.I. 1989/1100.

Status: *This is the original version (as it was originally made). This item of legislation is currently only available in its original format.*

EXPLANATORY NOTE

This note is not part of the Regulations)

These Regulations amend the Schedule to the Design Right (Semiconductor Topographies) Regulations 1989 to give effect to a communication from the European Commission pursuant to paragraph 3 of Article 1 of Council Decision [88/311/EEC](#) (O.J. No. L140, 7.6.1988, page 13). This communication determines that Sweden is a country which satisfies the conditions in paragraph 2 of that Article that protection of topographies of semiconductor products under Swedish law has been extended to all classes of persons in the Member States of the European Economic Community.